

Fig EV4 A

IP,anti-FLAG

Vector  
FLAG-HA-Ago1  
FLAG-HA-Ago1x

-180

Anti-GW182

Vector  
FLAG-HA-Ago1  
FLAG-HA-Ago1x

-95

Anti-HA

Input

Vector  
FLAG-HA-Ago1  
FLAG-HA-Ago1x

-180

Anti-GW182

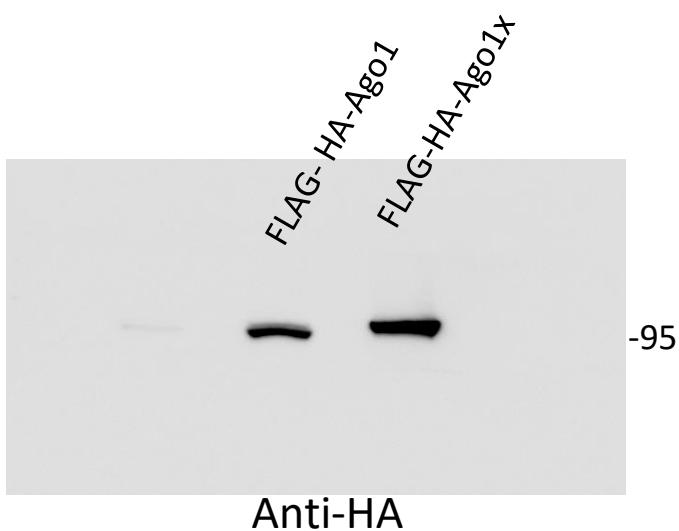
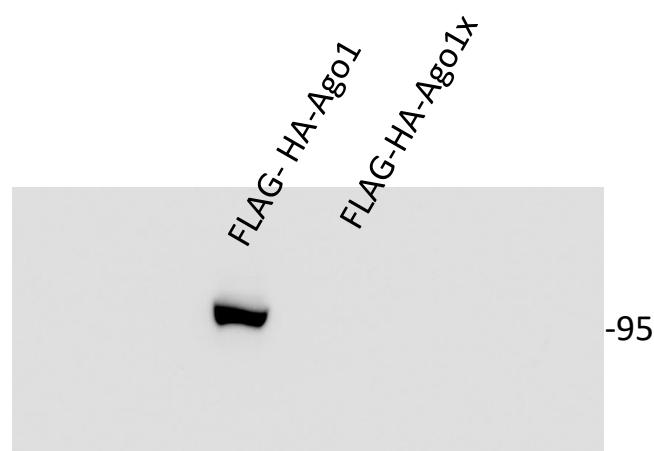
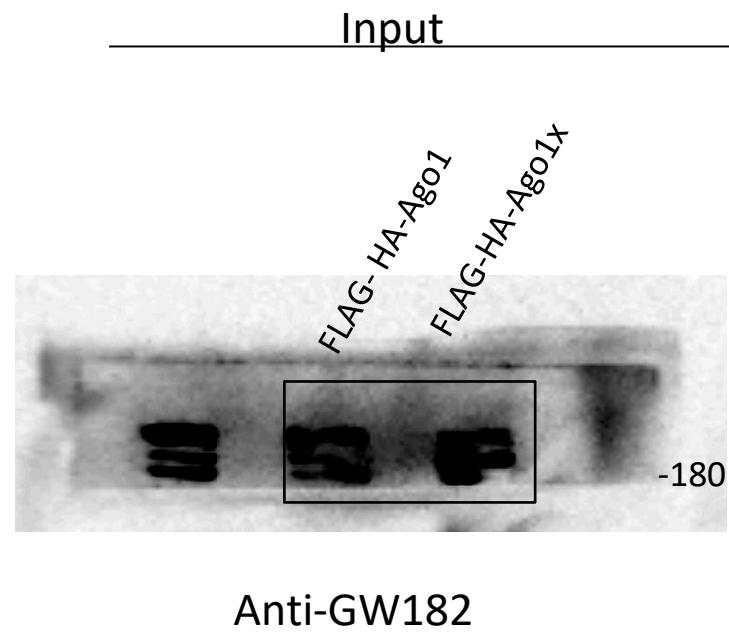
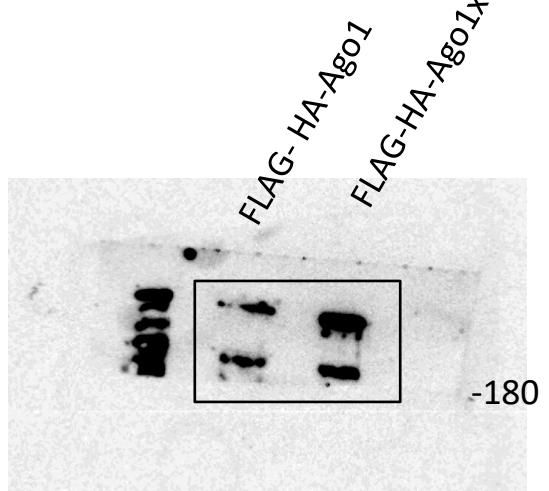
Vector  
FLAG-HA-Ago1  
FLAG-HA-Ago1x

-95

Anti-HA

Fig EV4 B

IP,anti-GW182



FIG\_EV4\_C